

FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

Applicant:
Case:
Serial No.:
Filing Date:
Group:

Azadet, E.F. Haratsch
13-5
09/834,668
April 13, 2001
2152

U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
	5,870,433	2/9/99	Huber et al.		
	6,035,006	3/7/00	Matui		
	6,201,831	3/13/01	Agazzi et al.		

FOREIGN PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO

OTHER DOCUMENTS

EXAMINER	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	1.	Keshab K. Parhi, "Pipelining in Algorithms with Quantizer Loops," IEEE Transactions on Circuits and Systems, Vol. 38. No. 7, 745-754 (July 1991)

2. Bednarz et al., "Design, Performance, and Extensions of the RAM-DFE Architecture," IEEE Transactions on Magnetics, Vol. 31, No. 2, 1196-1201 (March 1995)

RECEIVED
AUG 13 2001
Technology Center 2100

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.